

**NOIDA INTERNATIONAL UNIVERSITY**  
**TIME TABLE- 4th SEMESTER (2<sup>nd</sup> Year)-2020-21**  
**BRANCH: ELECTRONICS & COMMUNICATION ENGINEERING**

**Room No:208B**

DAY	9:30-10:10	10:20-11:00	11:10-11:50	12:00-12:40	1:40-2:20	2:30-3:10	3:20-4:00
MONDAY	Analog Electronics	Microcontroller	Library			Human values(N206)	student interaction with mentor
TUESDAY		Analog Electronics Lab	Python	Analog Electronics		Microcontroller	sports
WEDNESDAY		Analog and digital communications	Python		biology(N308B)	Python Lab	
THURSDAY	Analog Electronics	Human values(N206)	Analog and digital communications		Analog and digital communications Lab		biology(N308B)
FRIDAY		Microcontroller	Analog and digital communications	Human values(N206)	Microcontroller Lab		biology(N308B)

S.NO	SUBJECT CODE & CREDIT	SUBJECTS NAME	SUBJECT CREDIT	FACULTY NAME
1	EE07	Analog and digital communication	3	Mr Avinash K Singh
2	EE08	Analog and digital communication LAB	1	Mr Avinash K Singh
3	EE09	Analog Electronics	3	Dr. Priyanka Mishra
4	EE10	Analog Electronics Lab	1	Dr. Priyanka Mishra
5	EE11	Microcontroller	3	Mr. Shivangi Bansal
6	EE12	Microcontroller Lab	1	Mr. Shivangi Bansal
7	HS02	Human values	0	Mr. K. Babbar Sir
8	HS03/20	Human values	2	Mr. Ashwari
9	HS02	Human values	3	Mr. Ojha

Note: \*WORKSHEET Paper A compulsory  
 CLASS CO-ORDINATOR: Mr. Avinash K Singh  
 HOD: Ms. Sujata Arora

**NOIDA INTERNATIONAL UNIVERSITY**  
**TIME TABLE- 6th SEMESTER (3rd Year)-2020**  
**BRANCH: ELECTRONICS & COMMUNICATION ENGINEERING**

**Room No:209A**

DAY	9:30-10:10	10:20-11:00	11:10-11:50	12:00-12:40	1:40-2:20	2:30-3:10	3:20-4:00
MONDAY	Computer Networks	Control system	Computer Networks Lab		Quality Management	student interaction with mentor	library
TUESDAY		Control system Lab	Quality Management			Mini Project	
WEDNESDAY	Computer Networks	EMV	Quality Management				
THURSDAY	Control system	EMV			Computer Networks Lab		
FRIDAY	Computer Networks	EMV		Control system		Mini Project	

S.NO	SUBJECT CODE & CREDIT	SUBJECTS NAME	SUBJECT CREDIT	FACULTY NAME
1	EE19	control system	3	Mr Avinash K Singh
2	EE20	computer networks	3	Mr J.P. Bhatti
3	EE21/22	Quality management	2	Mr. Rishabh Verma
4	EE21	Quality Management	3	Mr. Lovkesh Sharma, Rashmi Verma
5	EE22	Mini Project	2	Mr. Shivangi Bansal
6	EE23	Control system Lab	1	Mr Avinash K Singh
7	EE24	computer network Lab	2	Mr J.P. Bhatti

Note: \*WORKSHEET Paper A compulsory  
 CLASS CO-ORDINATOR: Ms. Rashmi Verma  
 HOD: Ms. Sujata Arora

**NOIDA INTERNATIONAL UNIVERSITY**  
**TIME TABLE- 8th SEMESTER (4th Year)-2020**  
**BRANCH: ELECTRONICS & COMMUNICATION ENGINEERING**

**Room No:209B**

DAY	9:30-10:10	10:20-11:00	11:10-11:50	12:00-12:40	1:40-2:20	2:30-3:10	3:20-4:00
MONDAY		Entrepreneurship Development	Wireless Communication	Wireless Communication	SPORTS	Artificial Neural Networks	Major project
TUESDAY	Entrepreneurship Development N:202	Entrepreneurship Development	Artificial Neural Networks	Artificial Neural Networks	LIBRARY		Major project
WEDNESDAY	Wireless Communication(ROOM NO. 309 A)	Entrepreneurship Development	Artificial Neural Networks	Wireless Communication	LIBRARY		Major project

S.NO	SUBJECT CODE & CREDIT	SUBJECTS NAME	SUBJECT CREDIT	FACULTY NAME
1	EE1-301	Wireless Communication	4	Dr. Priyanka Mishra
2	EE1-303	Artificial Neural Networks	4	Mr J.P. Bhatti
3	EE1-302	Entrepreneurship Development	4	Mr. Ashwari aryanav
4	EE1-300	Major Project	8	Dr. Priyanka Mishra

Note: \*WORKSHEET Paper A compulsory  
 CLASS CO-ORDINATOR: Dr. Priyanka Mishra  
 HOD: Sujata Arora

**NOIDA INTERNATIONAL UNIVERSITY**  
**TIME TABLE- M.Tech VLSI DESIGN (Second Semester-2020)**  
**BRANCH: ELECTRONICS & COMMUNICATION ENGINEERING**

DAY	9:30-10:10	10:20-11:00	11:10-11:50	12:00-12:40	1:40-2:20	2:30-3:10	3:20-4:00
Monday						ANALOG AND DIGITAL CMOS DESIGN LAB	
WEDNESDAY	COMB. BUSES & INTERFACES(ROOM NO.209H)		Audit course-room no.-309(B)	VLSI DESIGN VERIACEST.	LOW POWER VLSI DESIGN	MINI PROJECT	VLSI DESIGN VERIACEST. LAB
THURSDAY	COMB. BUSES & INTERFACES(ROOM NO.209H)	LOW POWER VLSI DESIGN(ROOM NO.209 H)	Audit course-room no.-309(B)	ANALOG AND DIGITAL CMOS DESIGN	VLSI DESIGN VERIACEST	COMB. BUSES & INTERFACES(ROOM NO.209H)	ANALOG AND DIGITAL CMOS DESIGN
Friday	LOW POWER VLSI DESIGN(ROOM NO.209 H)		VLSI DESIGN VERIACEST.	ANALOG AND DIGITAL CMOS DESIGN	MINI PROJECT		VLSI DESIGN VERI & TESTING LAB

S.NO	SUBJECT CODE	SUBJECTS NAME	SUBJECT CREDIT	FACULTY NAME
1	VPECT3	ANALOG AND DIGITAL CMOS DESIGN	3	Mr. Shivangi Bansal
2	VPECT2	ANALOG AND DIGITAL CMOS DESIGN LAB	2	Mr. Shivangi Bansal
3	VPECT4	VLSI DESIGN VERIFICATION AND TESTING	3	Mr. Owais A. Shah
4	VPECT4	VLSI DESIGN VERIFICATION AND TESTING LAB	2	Mr. Owais A. Shah
5	VPEL3	LOW POWER VLSI DESIGN	3	Mr. Owais A. Shah
6	VPEL2	COMMUNICATION BUSES AND INTERFACES	3	Mr. Rashmi Verma
7	MAC02	**ALERT COURSE 2-Dissater Management	2	Mr. Parthiv aryanav
8	MAC02	MINI PROJECT	2	Respective guide

\*\* JOINT CLASS FOR ALL BRANCHES  
 Class coordinator: Mr. Owais  
 HOD: Ms. Sujata Arora

**NOIDA INTERNATIONAL UNIVERSITY**  
**TIME TABLE- M.Tech INFORMATION AND COMMUNICATION TECHNOLOGIES(REGULAR)(Second Semester-2020)**  
**BRANCH: ELECTRONICS & COMMUNICATION ENGINEERING**

DAY	9:30-10:10	10:20-11:00	11:10-11:50	12:00-12:40	1:40-2:20	2:30-3:10	3:20-4:00
WEDNESDAY	COMB. BUSES & INTERFACES(ROOM NO.209H)	ADV. ALGORITHM- room no.212 A	Audit course-room no.-309(B)	ADV. ALGORITHM LAB	LOW POWER VLSI DESIGN	SOFT COMPUTING- room no.212 A	MINI PROJECT
THURSDAY	COMB. BUSES & INTERFACES(ROOM NO.209H)	LOW POWER VLSI DESIGN(ROOM NO.209 H)	Audit course-room no.-309(B)		MINI PROJECT	COMB. BUSES & INTERFACES(ROOM NO.209H)	SOFT COMPUTING- room no.212 A
FRIDAY	LOW POWER VLSI DESIGN(ROOM NO.209 H)	ADV. ALGORITHM- room no.212 A	SOFT COMPUTING- room no.212 A	SOFT COMPUTING LAB	ADV. ALGORITHM- room no.212 A	ADV. ALGORITHM LAB	SOFT COMPUTING LAB

S.NO	SUBJECT CODE&CREDIT	SUBJECTS NAME	SUBJECT CREDIT	FACULTY NAME
1	SPCT1	**ADVANCE ALGORITHM	3	Mr.Shanti Kalia
2	SPCT1	**ADVANCE ALGORITHM LAB	2	Mr. Ashwari aryanav
3	SPCT4	**SOFT COMPUTING	3	Mr J.P. Bhatti
4	SPCT4	**SOFT COMPUTING LAB	2	Mr J.P. Bhatti
5	VPEL3	LOW POWER VLSI DESIGN	3	Mr. Owais Ahmad Shah
6	VPEL2	COMMUNICATION BUSES AND INTERFACES	3	Mr. Rashmi Verma
7	**ALERT COURSE 2-Dissater Management		2	Mr. Parthiv aryanav
8	MINI PROJECT		2	Mr. Avinash M. Rashmi

\*\* JOINT CLASS WITH CSE  
 \*\* JOINT CLASS FOR ALL BRANCHES  
 Class coordinator: Mr. Rashmi verma  
 HOD: Ms. Sujata Arora